

U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10067425	02/04/2002	438	126	2812	jeper

****APPLICANTS:** Van Veen Nicolaas; De Samber Marc; Weiss Thomas;

**CONTINUING DATA VERIFIED:

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**** FOREIGN APPLICATIONS VERIFIED:**

EUROPEAN PATENT OFFICE (EPO) 01200496.6 02/09/2001

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO NL 010069	
TITLE : Method of manufacturing a semiconductor device and a semiconductor device obtained by means of said method			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims		Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheet to Draw.	Fig. to Draw.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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